

Fig. 1



Fig. 2A

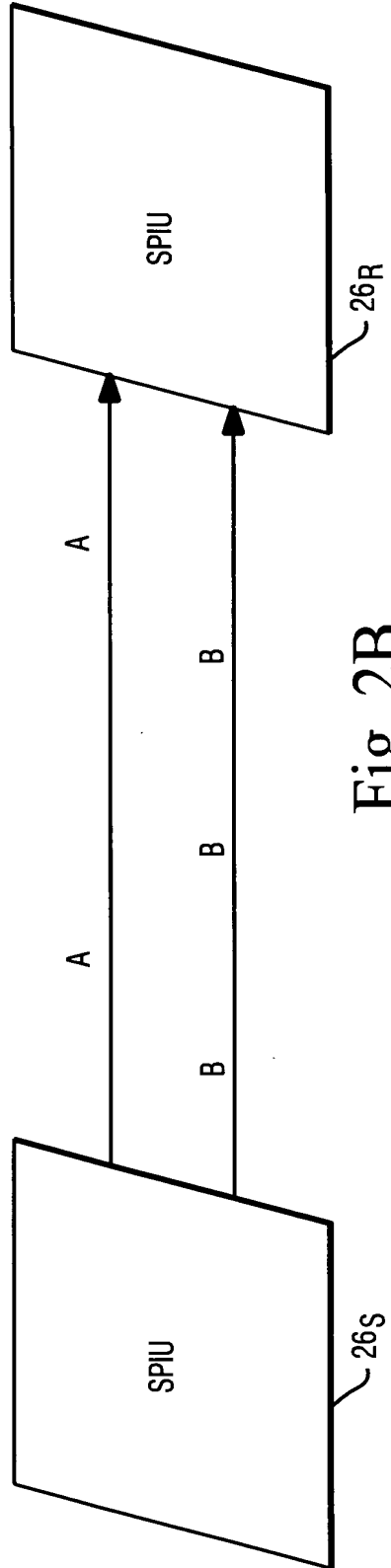


Fig. 2B



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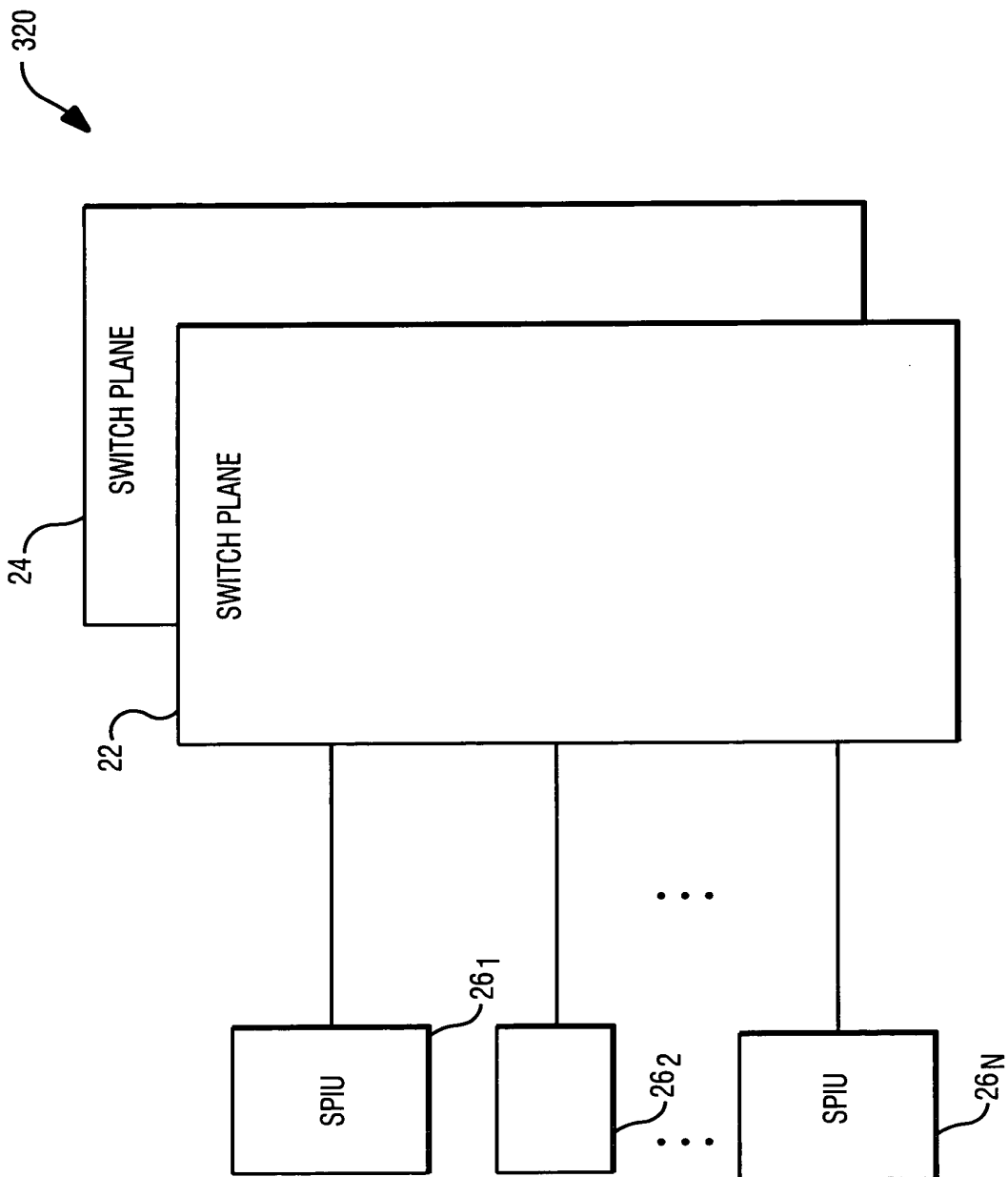


Fig. 3

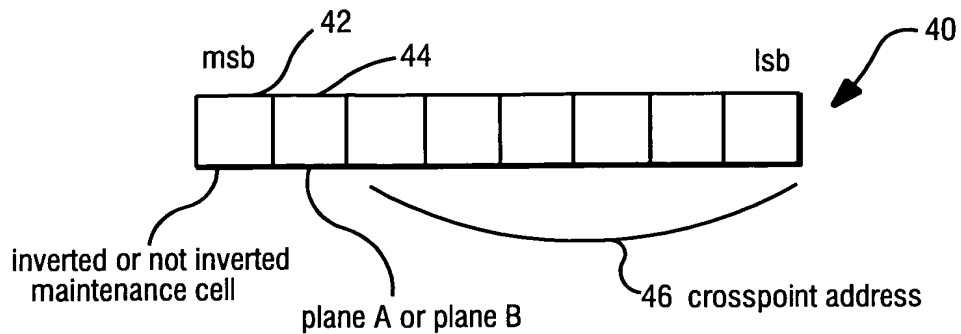


Fig. 4A

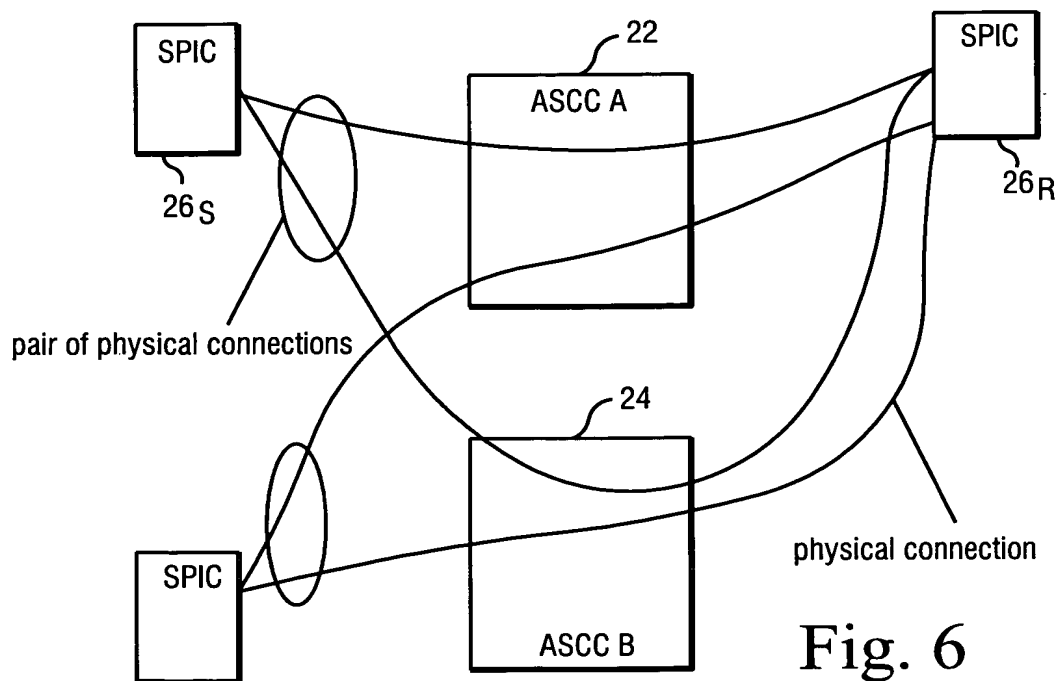


Fig. 6



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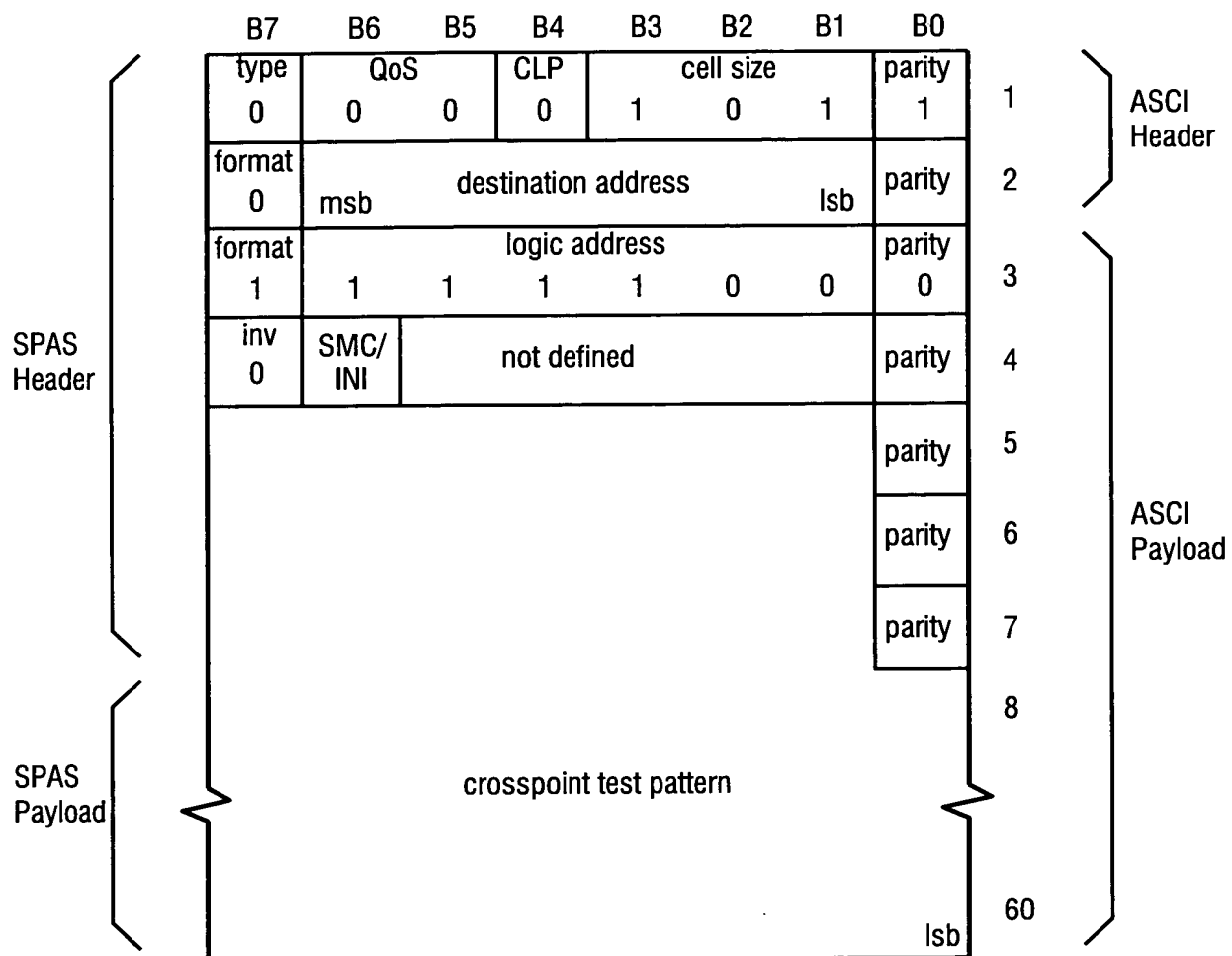


Fig. 4B(1) Initialisation Cell

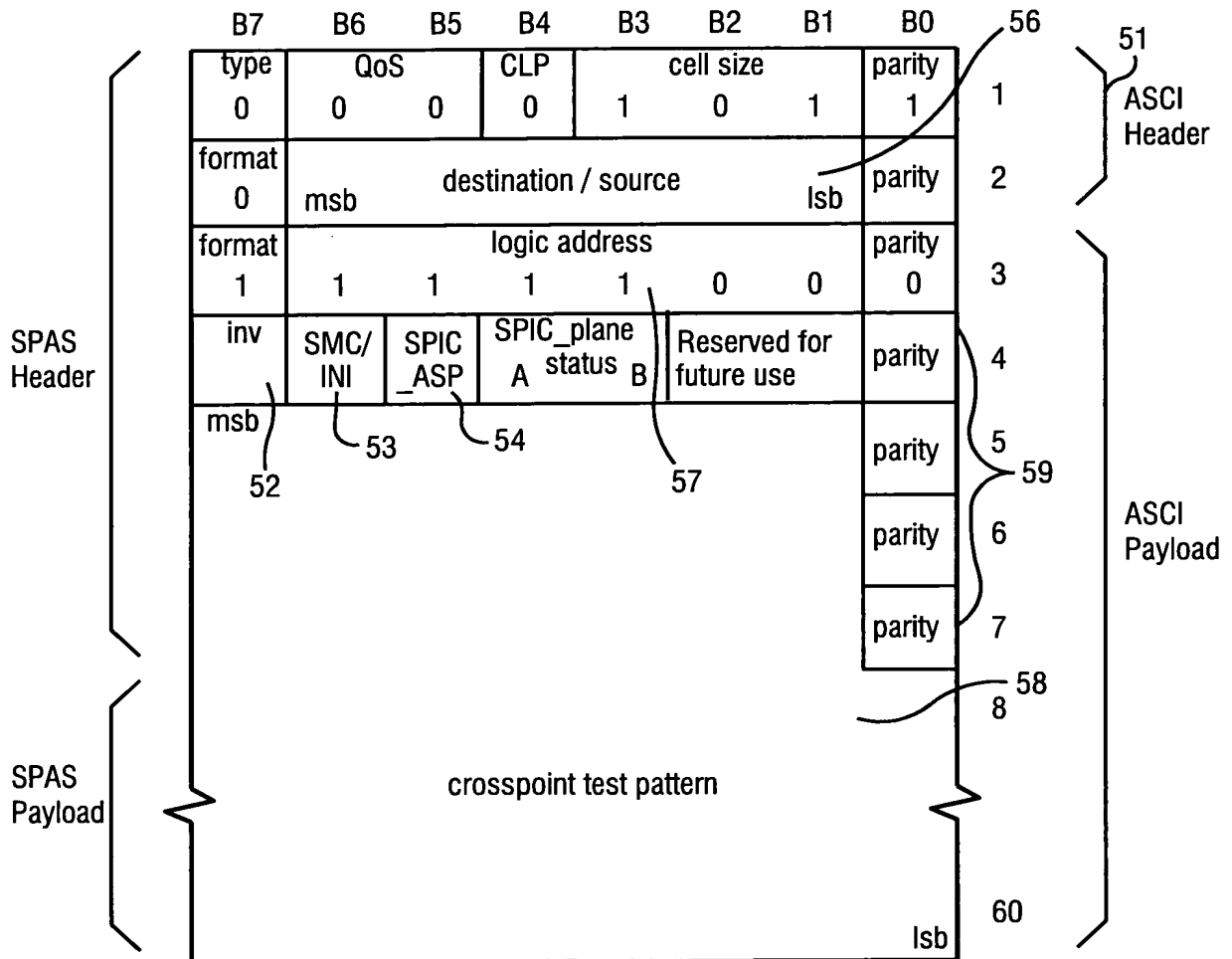


Fig. 4B(2)

Switch Maintenance Cell

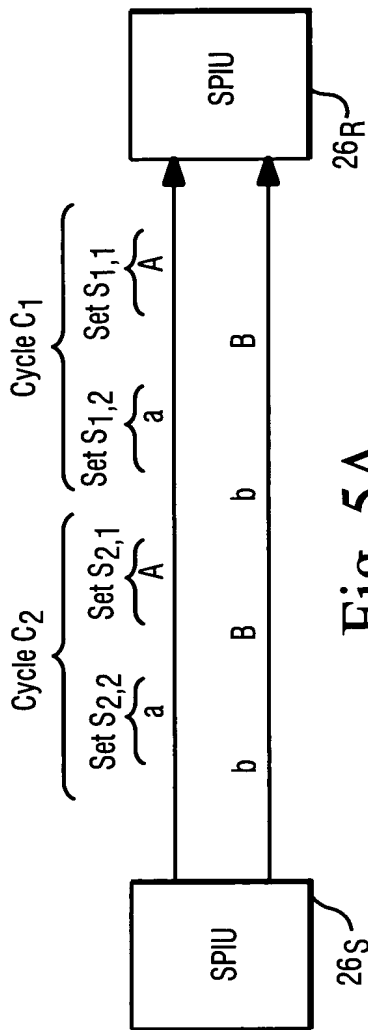


Fig. 5A

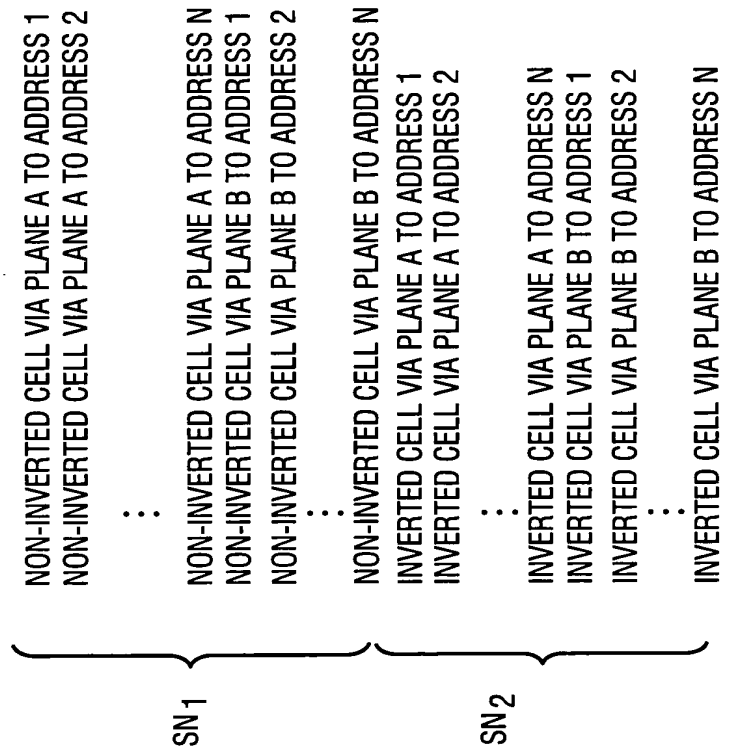


Fig. 5B

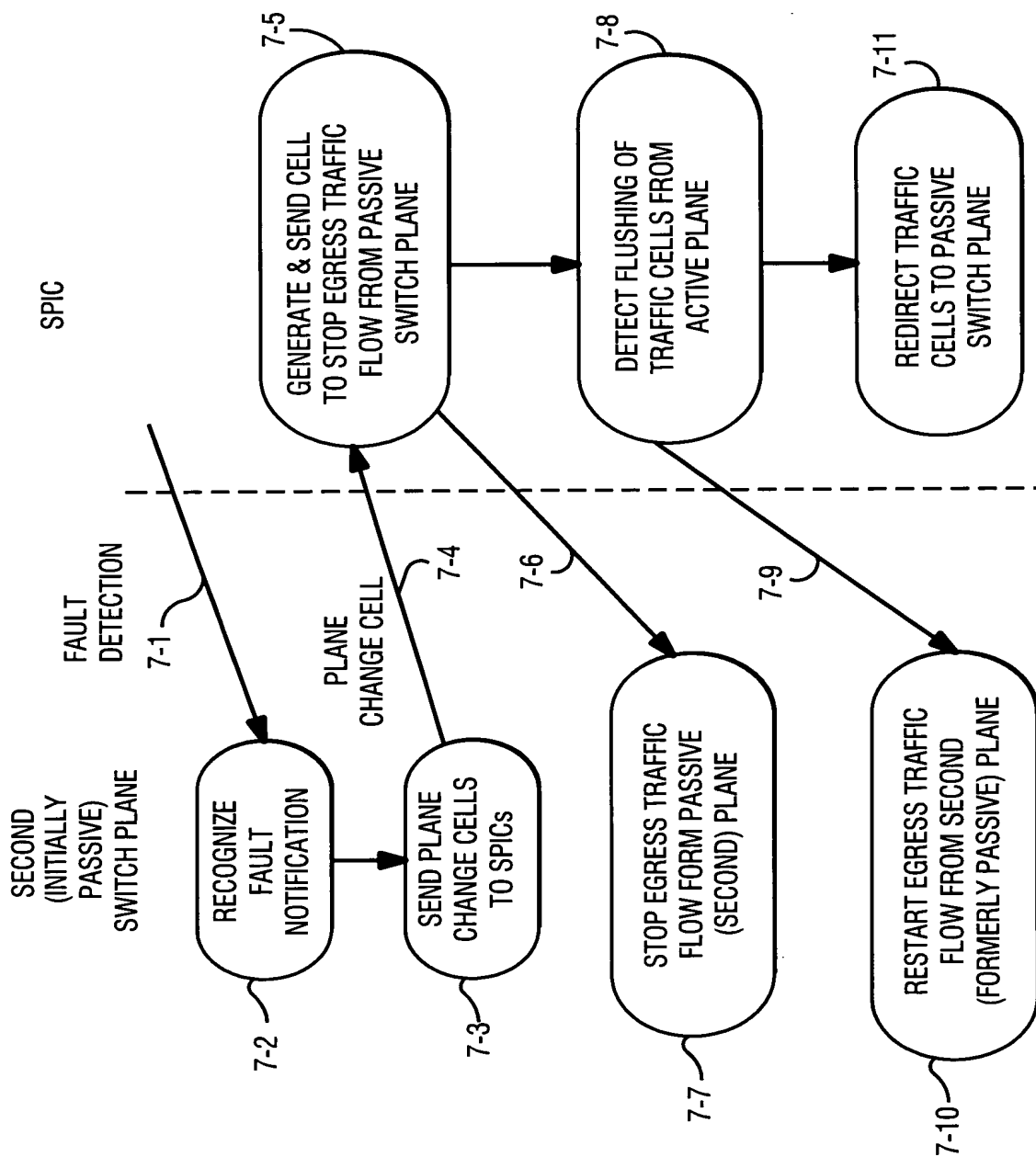


Fig. 7

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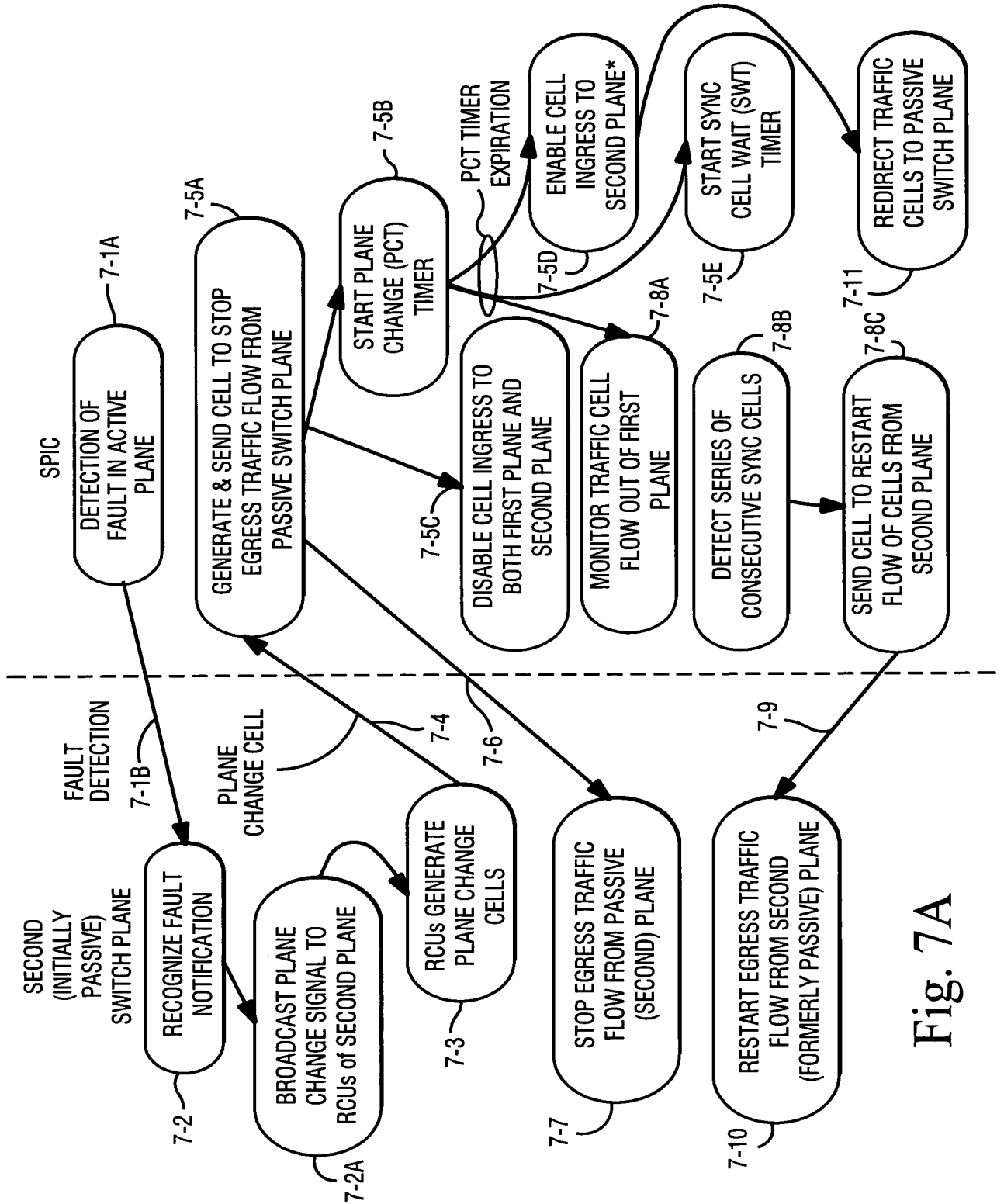


Fig. 7A



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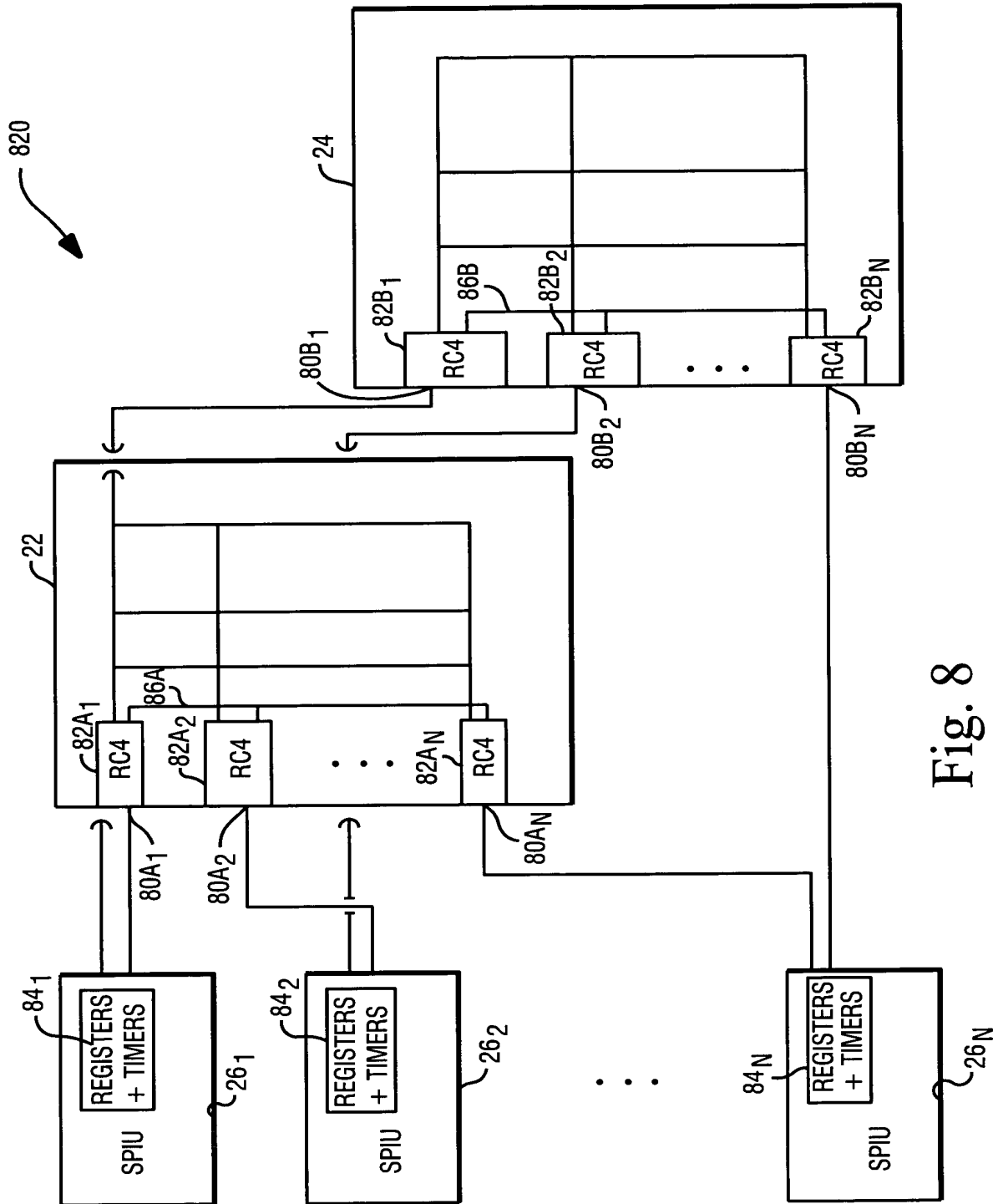


Fig. 8

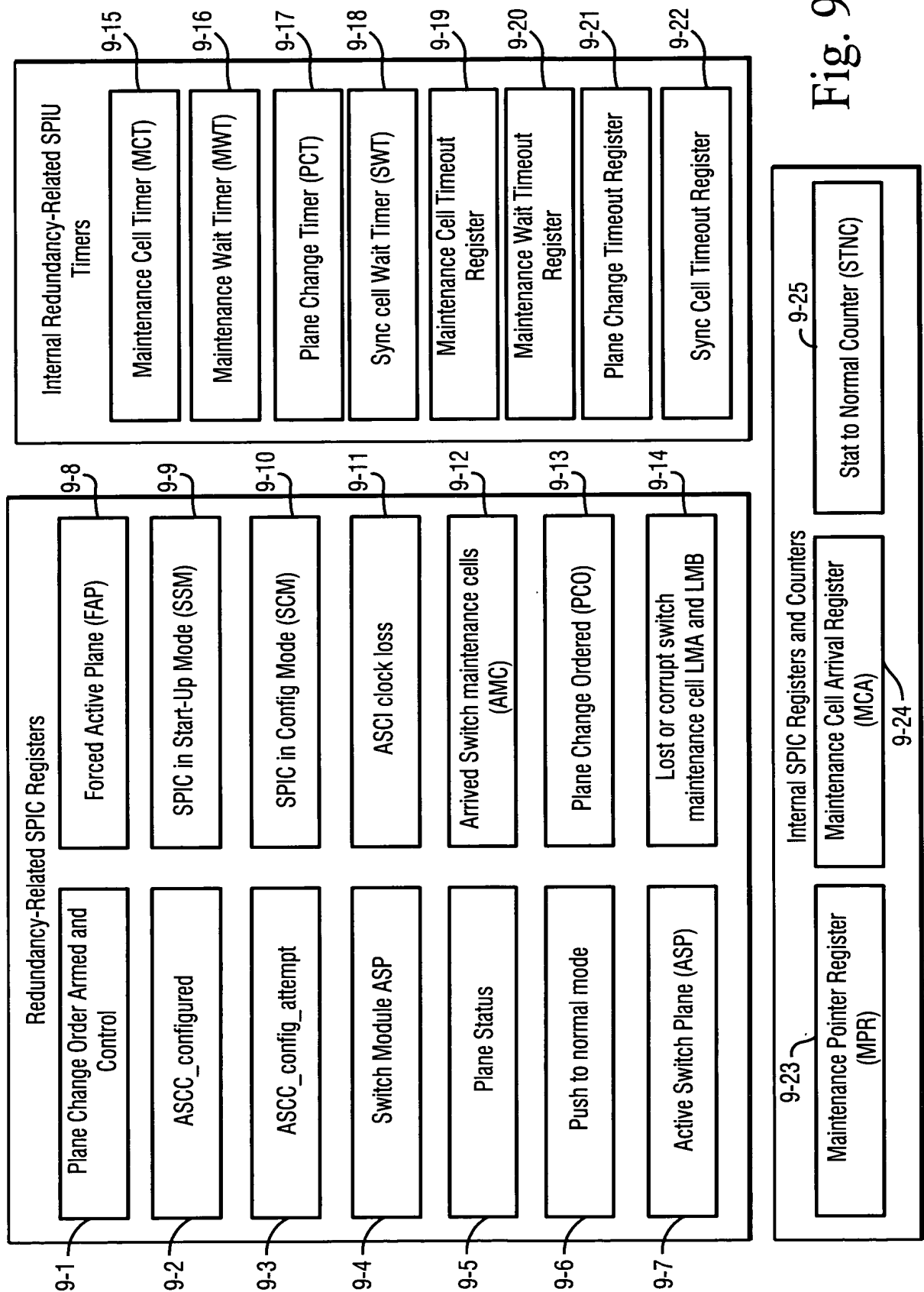


Fig. 9



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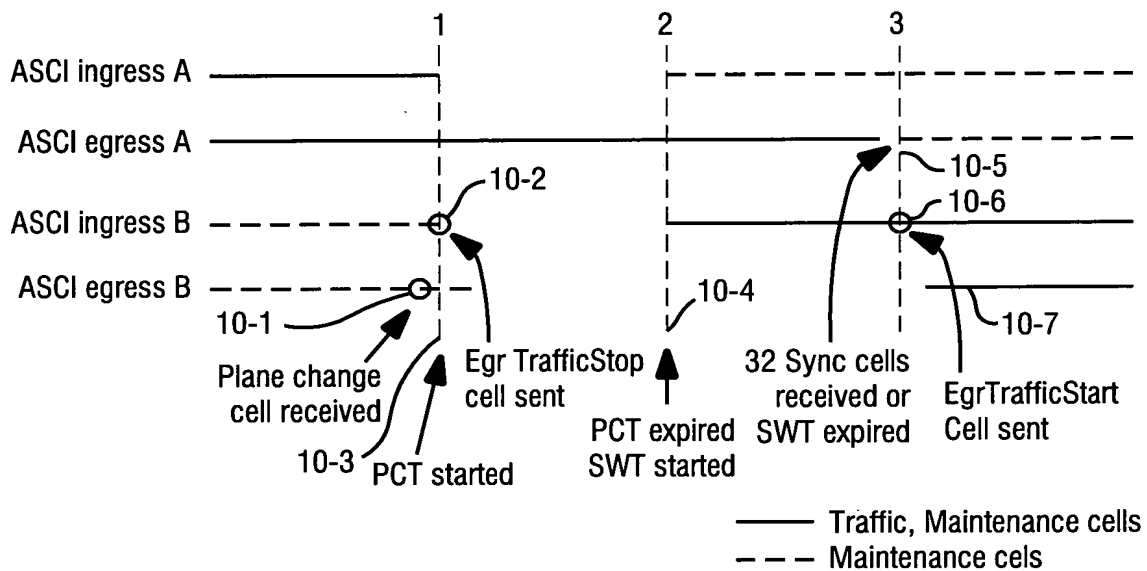


Fig. 10

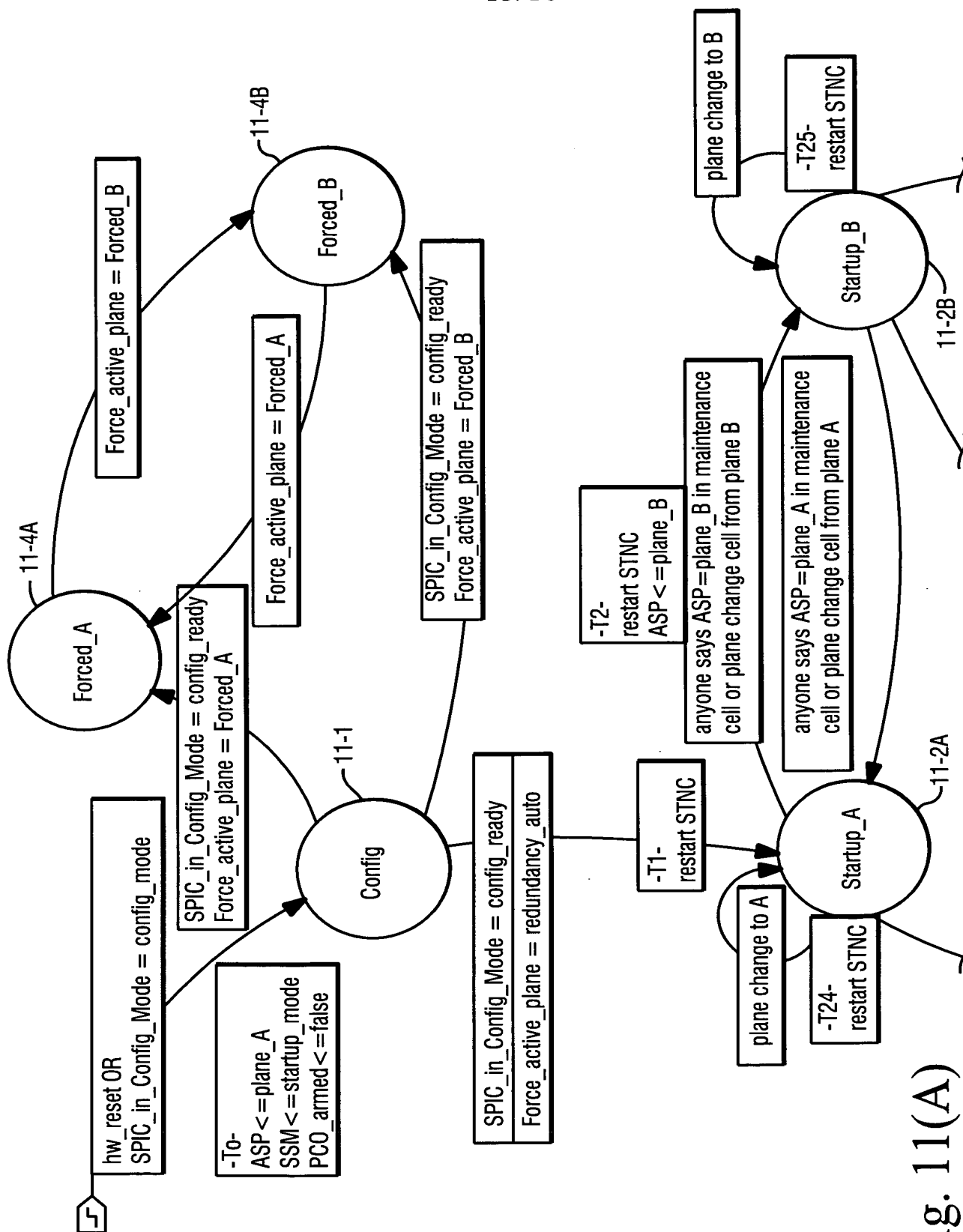
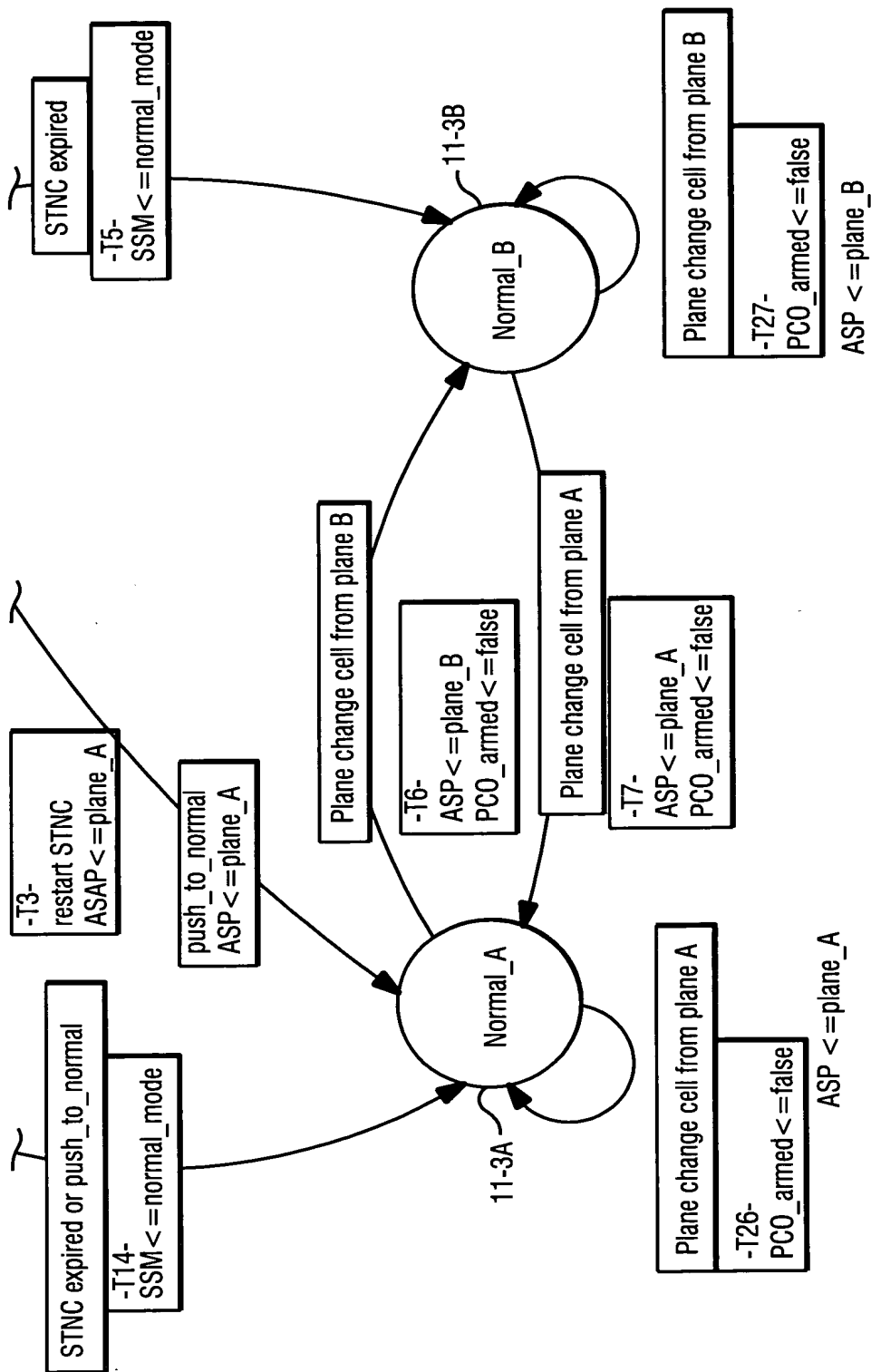


Fig. 11(A)



ASP = Active switch plane
 SSM = SPIC in start-up mode
 PCO_armed = Plain change order armed
 STNC = Start to normal counter

Fig. 11(B)

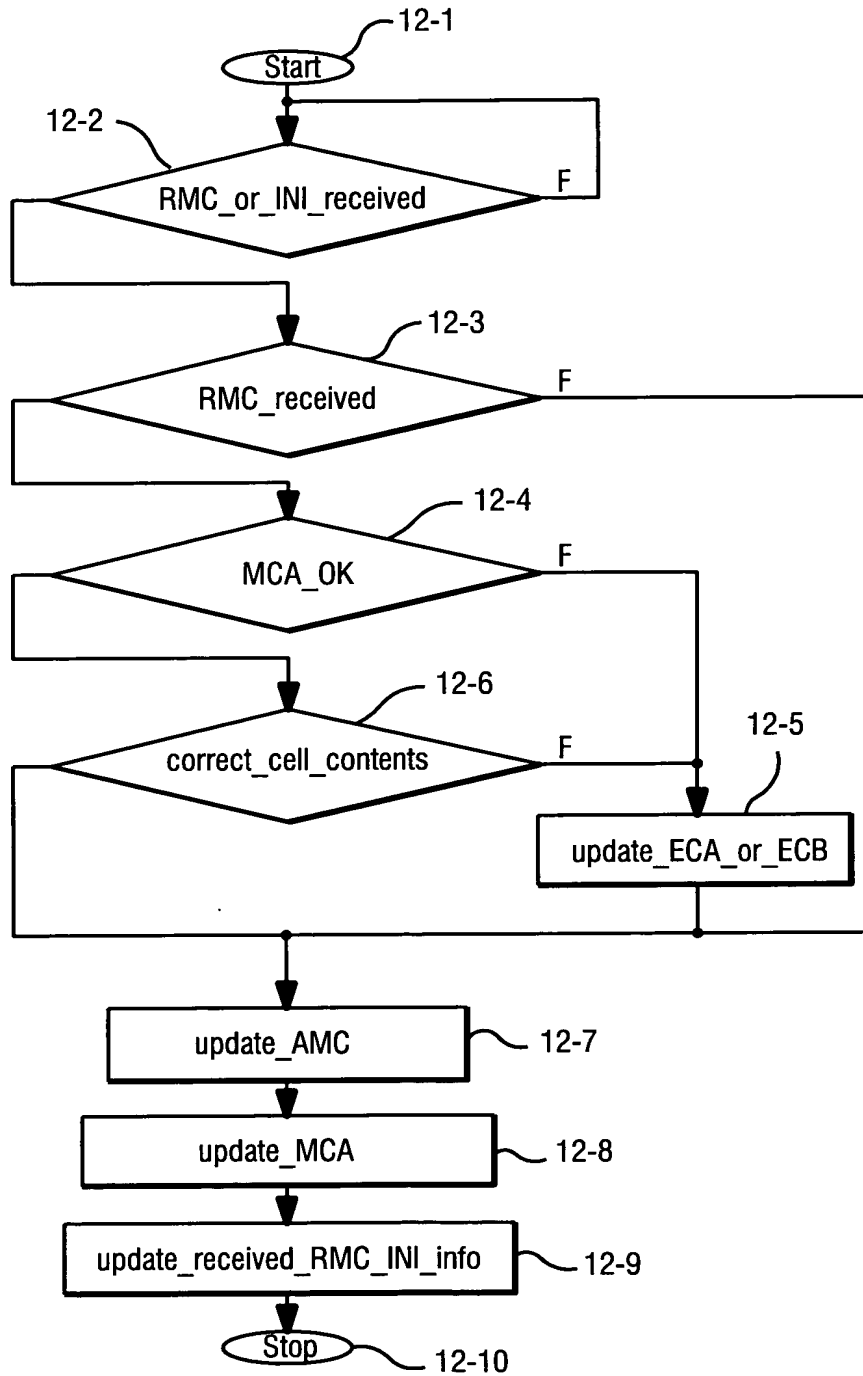


Fig. 12



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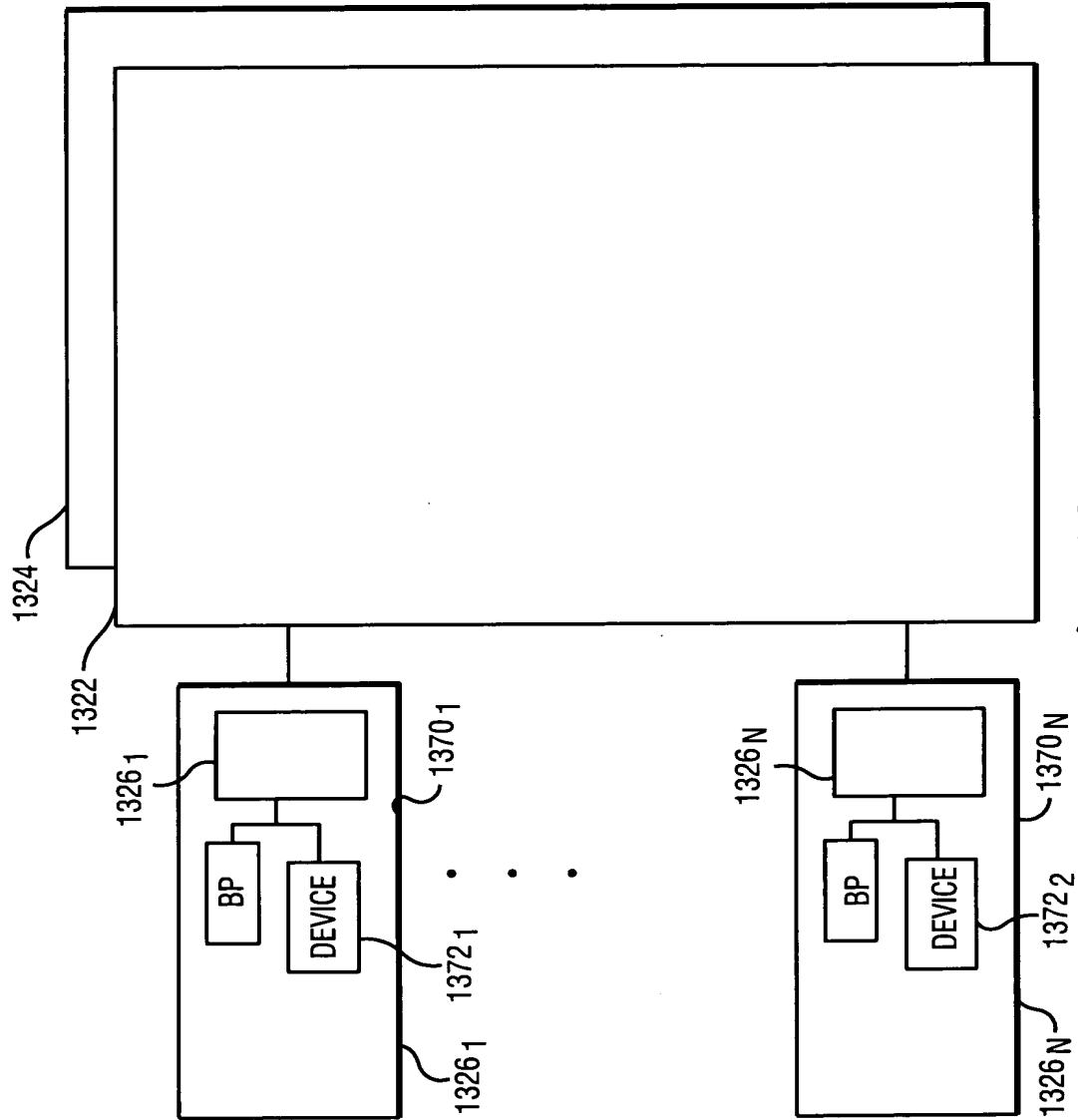


Fig. 13